



Japan Patent Office



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## REGIONAL SEMINAR

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## Effective Utilization of Patent Classification Systems

organized by  
World Intellectual Property Organization (WIPO)

in cooperation with  
Japan Patent Office (JPO)

**Tokyo, December 11 and 12, 2013**

### LIST OF SPEAKERS

*prepared by the International Bureau of WIPO*

## KYUNG SHIK YANG

Mr. Yang is a Deputy Director, Patent Examination Policy Division, Patent Examination Policy Bureau, Korean Intellectual Property Organization, Daejeon. He is currently IPC coordinator in chemistry. Earlier, he was a Patent Examiner in the area of Environment and Energy; and was in the technical field of H01M, fuel cells and secondary batteries.

He was a PostDoc. at Delft University of Technology (in the Netherlands), during 2005-2006; and a Graduate Researcher at POSTECH Catalyst Technology Ce during 2004-2005.

He holds Ph.D. in Chemical Engineering from Pohang University of Science and Technology (POSTECH), Korea, besides M.S. in Chemical Engineering and B.S. in Chemical Engineering from the same University.

## ZHENG JIE (MS.)

Ms. Zheng Jie is a Deputy Director, Patent Documentation Department of the State Intellectual Property Office of the People's Republic of China, Beijing. She has been in the field of research on patent documentation and classification, and was concerned with patent classification cooperation projects (IP5 classification working group, WIPO IPC revision working group).

She holds Master of Engineering degree from Beijing Institute of Technology.

## YOSHITAKA OTA

Mr. Yoshitaka Ota is a Deputy Director, Patent Examination Policy Planning Office, Japan Patent Office, Tokyo.

He joined the Japan Patent Office in April, 2002 as an Assistant Examiner. He became Deputy Director of the Patent Examination Policy Planning Office of the Administrative Affairs Division in June, 2012.

He and his team are involved in developing patent classification policy, governing the patent classification internally and cooperating on patent classification with foreign patent offices or organizations internationally, for example participating in IPC Revision Working Group, IPC Committee of Experts of IPC Union and IP5 WG1 (Working Group on Classification). In addition, since his tenure in the Patent Examination Policy Planning Office in 2008, he has been involved in work sharing issues of search and examination among patent offices.

Furthermore, he has examined more than 2,200 national patent applications and provided more than 150 International Search Reports during career.

He obtained a Bachelor's Degree in Engineering from Tokyo University, Japan, in March 2002.

## TAKU FUKUMURA

Mr. Taku Fukumura is a Deputy Director in the Patent Examination Policy Planning Office, Japan Patent Office, Tokyo

He joined the Japan Patent Office in April, 2003 as an Assistant Examiner. He became Deputy Director in the Patent Examination Policy Planning Office of the Administrative Affairs Division in January, 2013.

He and his team are involved in maintaining and improving F-term classification and other search information (e.g.: the internal NPL databases and commercial databases which are available in JPO).

He obtained Master's Degree in Engineering from Hokkaido University, Japan, in March 2000.

## KOICHI MATSUSHITA

Mr. Koichi Matsushita is currently Senior Patent Classification Officer of the International Patent Classification (IPC) Section, World Intellectual Property Organization, Geneva.

One of the duties of this Section is preparing and publishing the new IPC versions by organizing the IPC Revision Working Group and the IPC Committee of Experts.

Before joining WIPO in 2009, he had worked for the Japan Patent Office for 13 years as a Patent Examiner in electrical field, as well as a Deputy Director of the Patent Classification Planning Section.

## KEN-ICHIRO NATSUME

Mr. Natsume has been the head of WIPO Japan Office since January 2012.

He joined the Japan Patent Office (JPO) in 1992 as a patent examiner. During this period, he became a visiting associate at the California Institute of Technology, USA, from 1996 to 1997. From 2000 to 2001, Mr. Natsume was with the Technology Research Division of JPO, dealing with various intellectual property issues including cutting edge topics such as business method patents and biotechnology related patents.

Mr. Natsume was posted in the First International Affairs Division of Ministry of Foreign Affairs of Japan (MOFA), from 2001 to 2003, where he covered international intellectual property issues including industrial property, copyright and plant variety. During his service at MOFA, he was engaged in Japan's accession of WIPO Performances and Phonograms Treaty (WPPT).

During his service at the Examination Standard Division of the JPO from 2004 to 2005, he was engaged in releasing several new examination standards of patents and utility models. He was also an appeal examiner at the JPO from 2009 to 2010 in the field of electronic image devices. He was also at the Permanent Mission of Japan to the International Organization in Geneva, from 2006 to 2009, where he was involved in various international intellectual property discussions and debates such as at WIPO, WTO/TRIPS and WHO.

Mr. Natsume was a director for multilateral policy division of JPO from 2010 to 2011 and played a leading role in the international discussions and negotiations including WIPO, WTO/TRIPS, WHO, CBD, Trilateral Meetings and IP5 Meetings as well as bilateral talks and negotiations with European and North American partners such as EU and USA.

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