AGENDA

1.   Opening of the session

2.   Adoption of the agenda

3.   Election of a Chair and two Vice-Chairs

4.   Report on the progress of the IPC revision program; Format of future IPC Revision
       Working Group meetings
            See projects CE 462 and CE 539.

5.   Report on the progress of the CPC and FI revision programs
            Reports by the EPO and the USPTO on the CPC and by the JPO on the FI.

6.   Report of the Expert Group on Semiconductor Technology (EGST)
            See projects CE 539 and CE 481.

7.   Use of trademarks in the IPC
            See projects CE 539 and M 815.

8.   Amendments to the Guide to the IPC and other basic IPC documents
            See projects CE 454, CE 455 and CE 539.

9.   Overview of IPC Working List Management Solution (IPCWLMS) and related issues
            See projects CE 492 and CE 539.

10.   AI-based IPC Reclassification – a potential replacement of "Default Transfer"
            See project CE 539.

11.   Report on IPC-related IT systems
            Presentation by the International Bureau.

12.   Experience from offices on computer-assisted (e.g., AI-based) classification
            Presentations by offices.

13.   Framework of technical competencies for patent classification
            See project CE 523.

14.   Adoption of the Report

15.   Closing of the Session

[End of Annex II and of document]