

AGENDA

1. Opening of the session
2. Election of a Chair and two Vice-Chairs
3. Adoption of the agenda
4. Modification of the Rules of Procedure of the Committee of Experts
See project [CE 549](#).
5. Report on the progress of the IPC revision program
See project [CE 462](#).
6. Report on the progress of the CPC and FI revision programs
Reports by the EPO and the USPTO on the CPC and by the JPO on the FI.
7. Report of the Expert Group on Semiconductor Technology (EGST)
See project [CE 481](#).
8. Amendments to the *Guide to the IPC* and other basic IPC documents
See projects [CE 454](#), [CE 455](#) and [CE 531](#).
9. The need for a new type of universal indexing/tagging scheme covering cross-cutting technologies
See project [CE 502](#).
10. Survey on the use of the IPC in terms of classification levels and other issues related to the IPC Working List Management Solution (IPCWLMS).
See projects [CE 492](#) and [CE 549](#).
11. AI-based IPC reclassification – a potential replacement of “Default Transfer”
See project [CE 532](#).
12. Other issues
See project [CE 549](#).
13. Report on IPC-related IT systems
Presentation by the International Bureau. See project [CE 501](#).
14. Experience from offices on computer-assisted (e.g., AI-based) classification
Presentations by offices. See project [CE 524](#).
15. Adoption of the report
16. Closing of the session

[End of Annex II and of document]